



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT

Application No.: 10/028,880) Date: March 10, 2004
)
Applicant: Ta-Ko Chuang, et al.) Examiner: Jeff H. Aftergut
) Group Art Unit 1733
Filed: 12/20/2001)
) Our Ref: 619416-6 B-4442
For: "Method for Bonding An Integrated)
Circuit Device...")

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This paper is filed in response to the Office Action dated October 10, 2003 and the Advisory Action dated January 14, 2004. A petition for a two-month extension of time to respond is attached, along with a check for the fee therefor. **All remarks herein are made without prejudice.**